

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: Toru TATSUMI, et al.  
Title: SEMICONDUCTOR DEVICE HAVING A MIS-  
TYPE FET, AND METHODS FOR  
MANUFACTURING THE SAME AND FORMING  
A METAL OXIDE FILM  
Appl. No.: 10/558,271  
International Filing Date: 5/31/2004  
371(c) Date: 10/16/2006  
Examiner: Jami M Valentine  
Art Unit: 2815  
Confirmation Number: 1477

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement set forth in the Office Action mailed December 07, 2007, Applicant hereby provisionally elects Group I for examination, without traverse.

Respectfully submitted,

Date 2/7/08

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